

Fig. 1 (PRIOR ART)

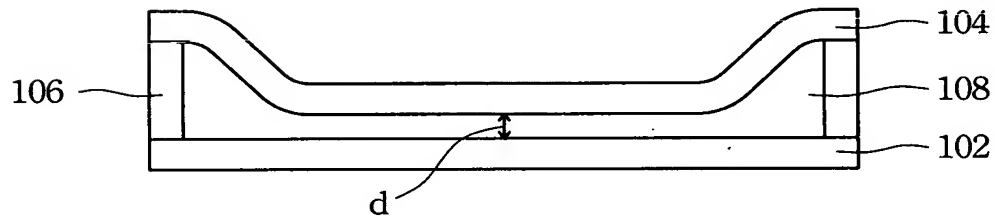


Fig. 2 (PRIOR ART)

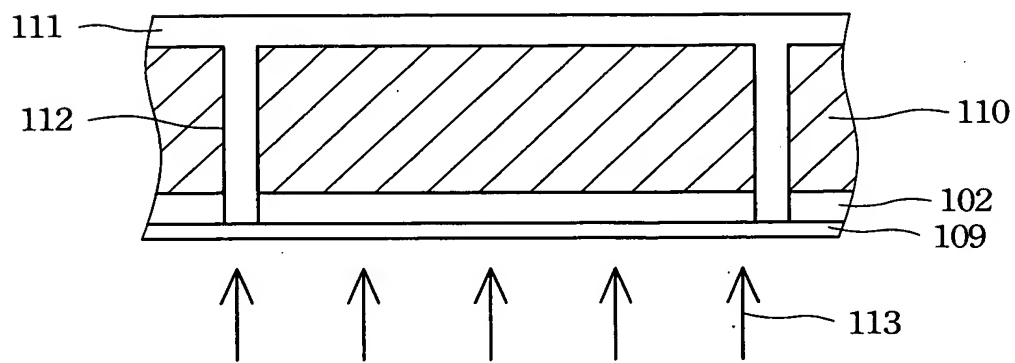


Fig. 3A (PRIOR ART)

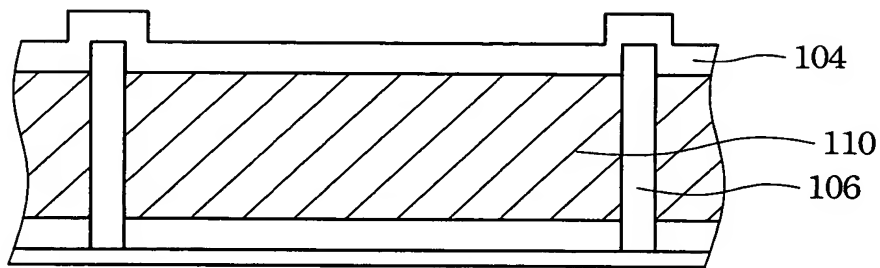


Fig. 3B (PRIOR ART)

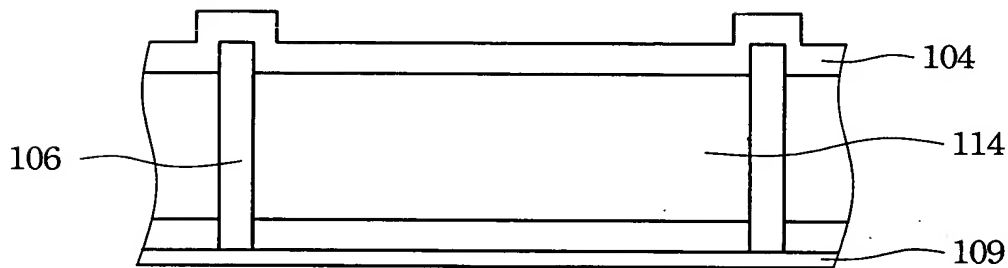


Fig. 3C (PRIOR ART)

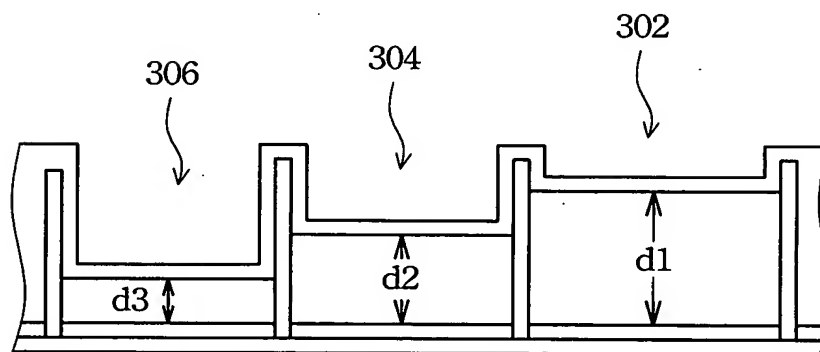


Fig. 4 (PRIOR ART)

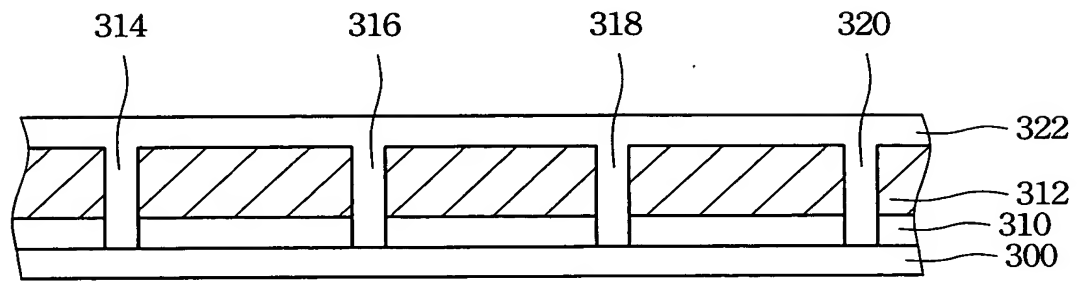


Fig. 5A (PRIOR ART)

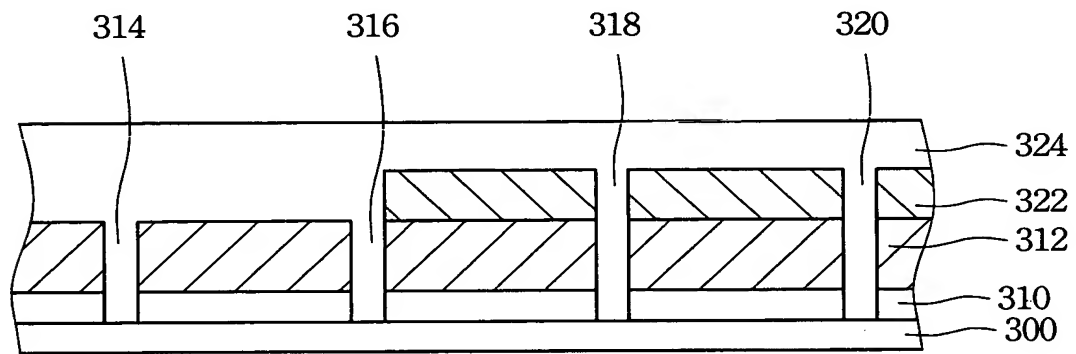


Fig. 5B (PRIOR ART)

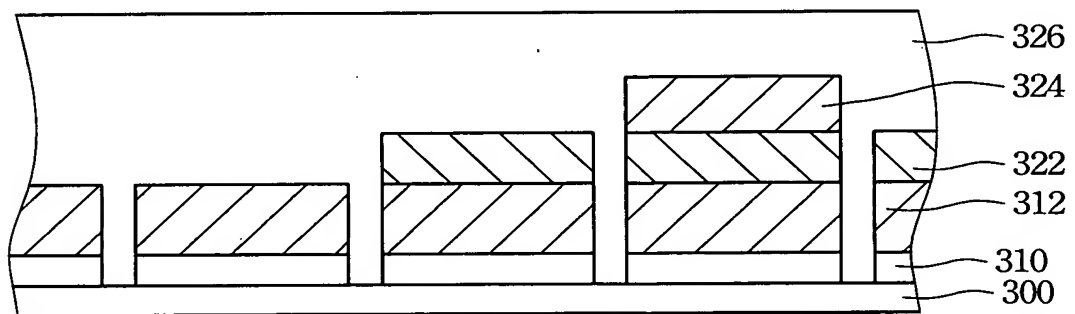


Fig. 5C (PRIOR ART)

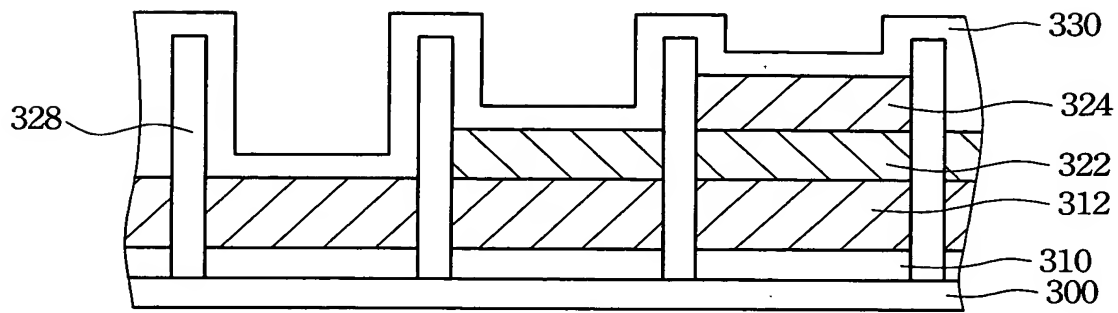


Fig. 5D (PRIOR ART)

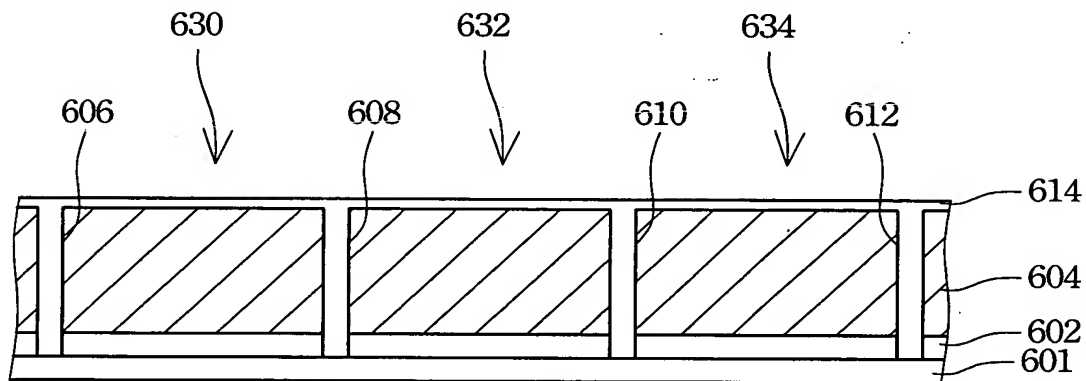


Fig. 6A

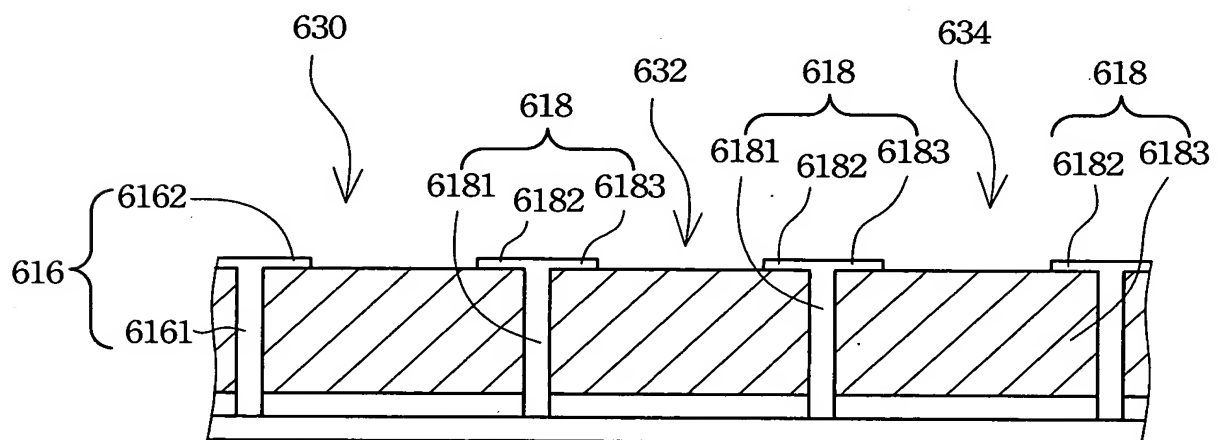


Fig. 6B

A METHOD FOR FABRICATING AN INTERFERENCE DISPLAY UNIT

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Docket No. 4006-275

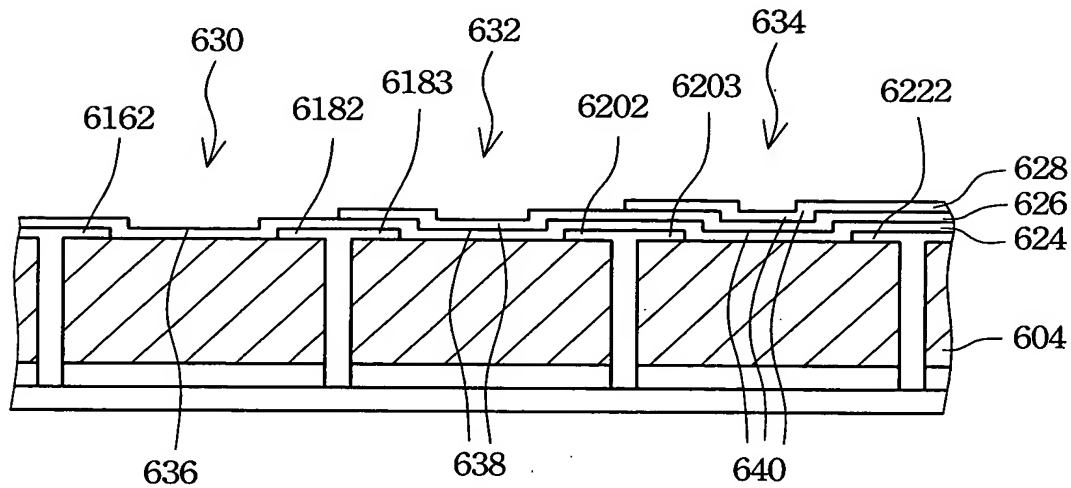


Fig. 6C

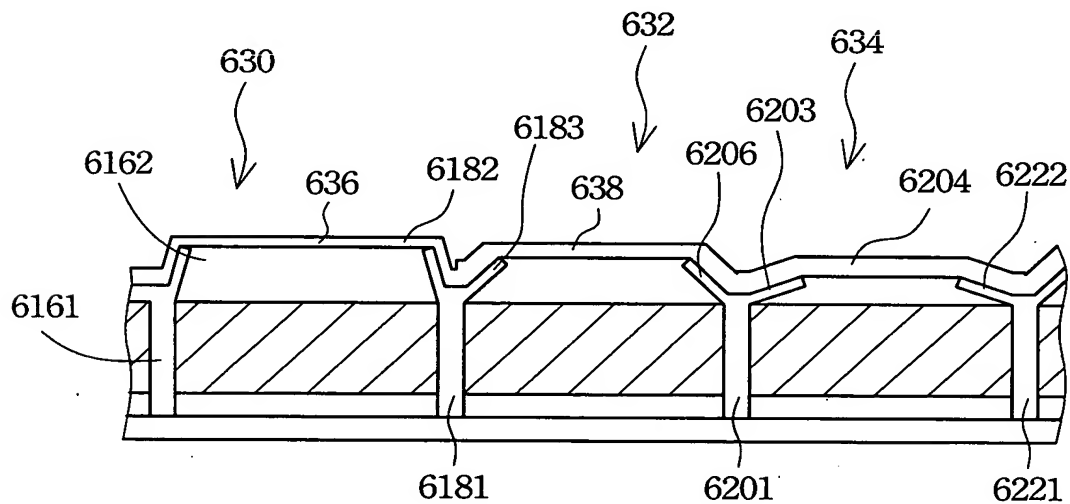


Fig. 6D

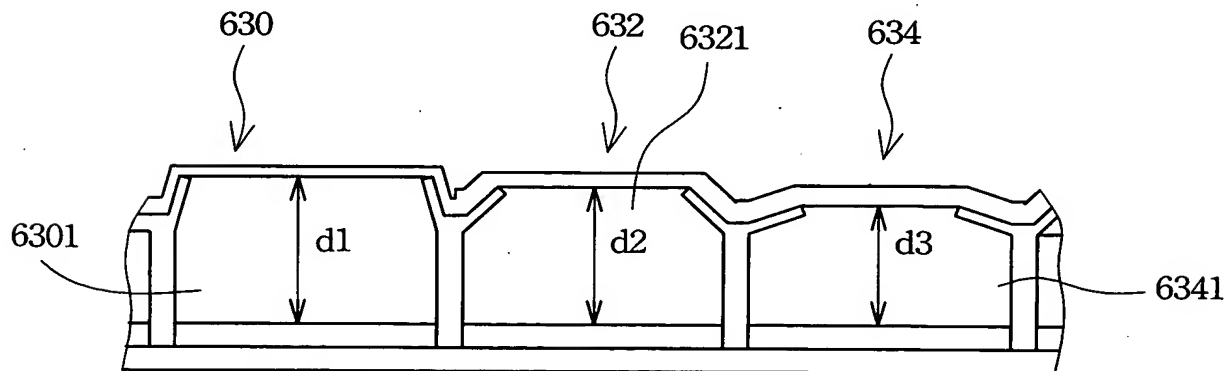


Fig. 6E